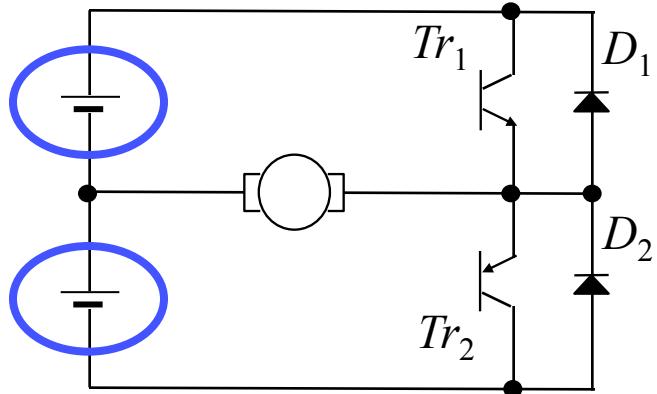


Power Electronics

No. 13: Full-bridge Inverter

Takeshi Furuhashi

Furuhashi_at_cse.nagoya-u.ac.jp



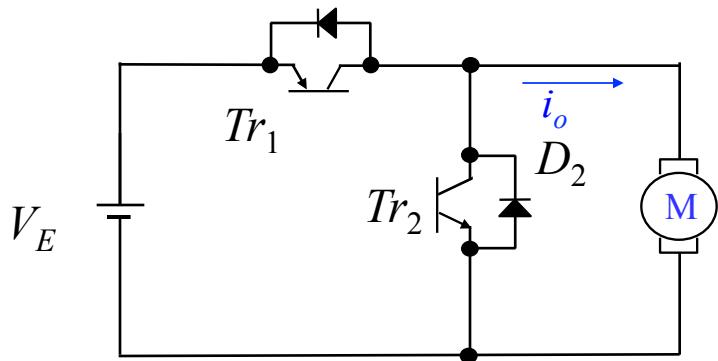
Half-bridge inverter

Two batteries are needed

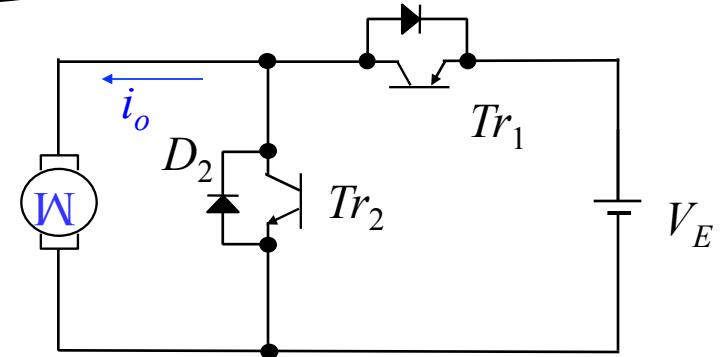


An inverter with one battery is more desirable.

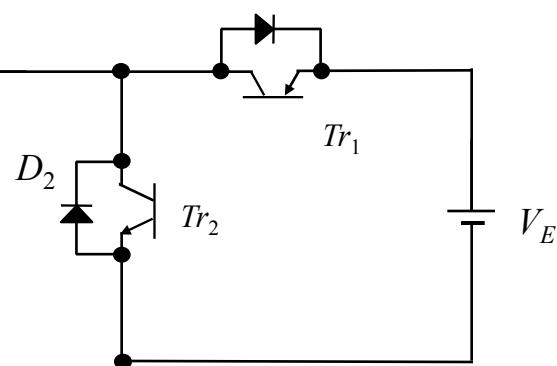
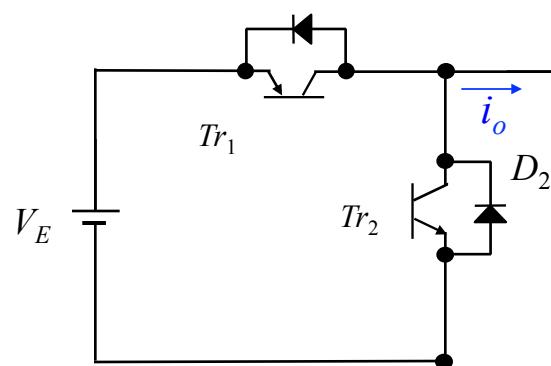
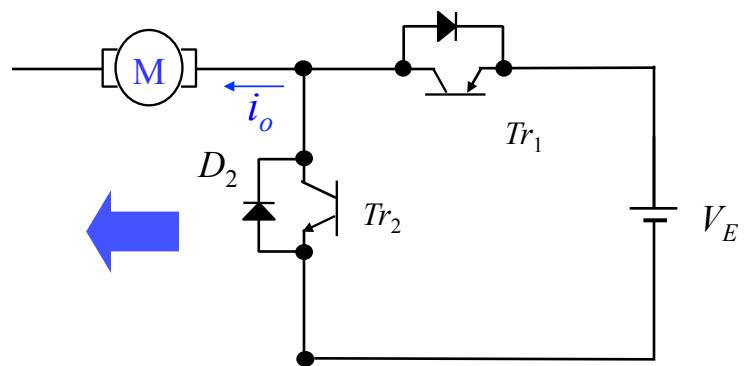
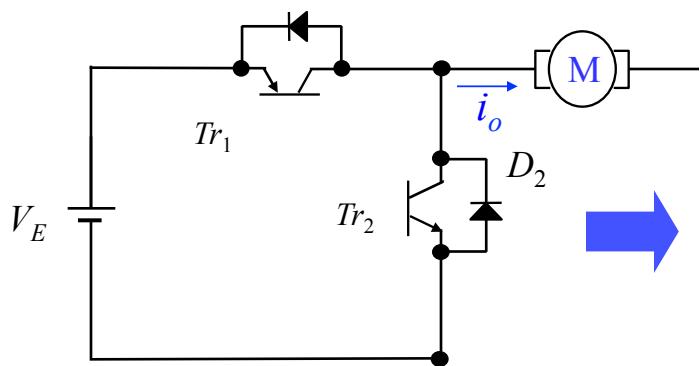
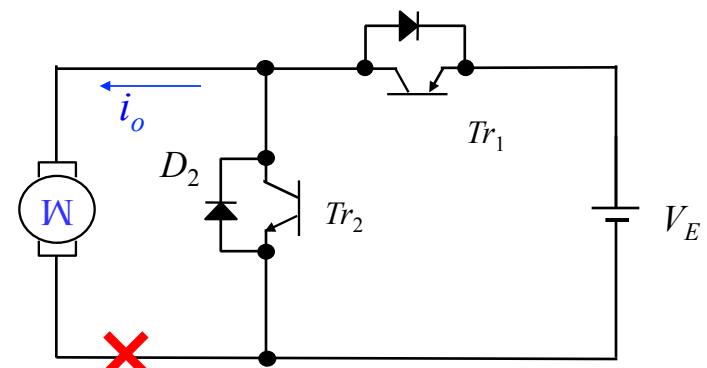
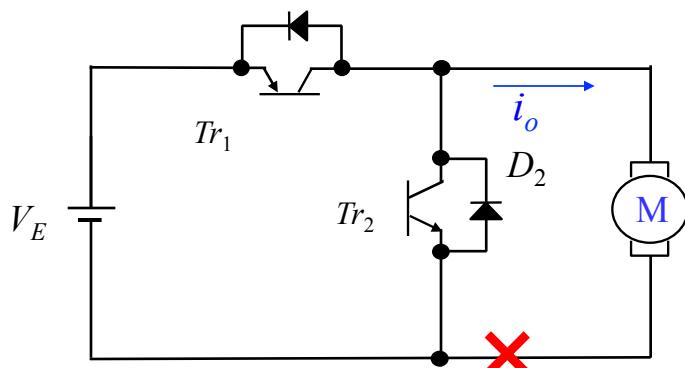
How can these be integrated?

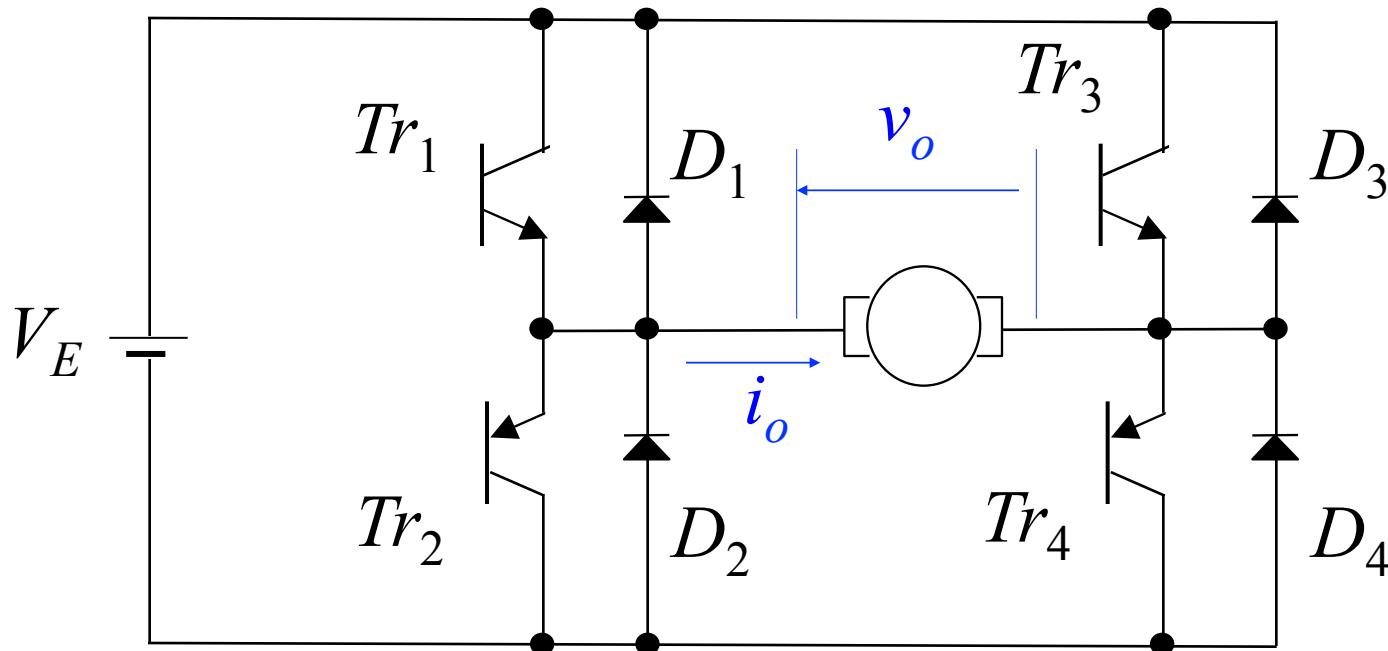
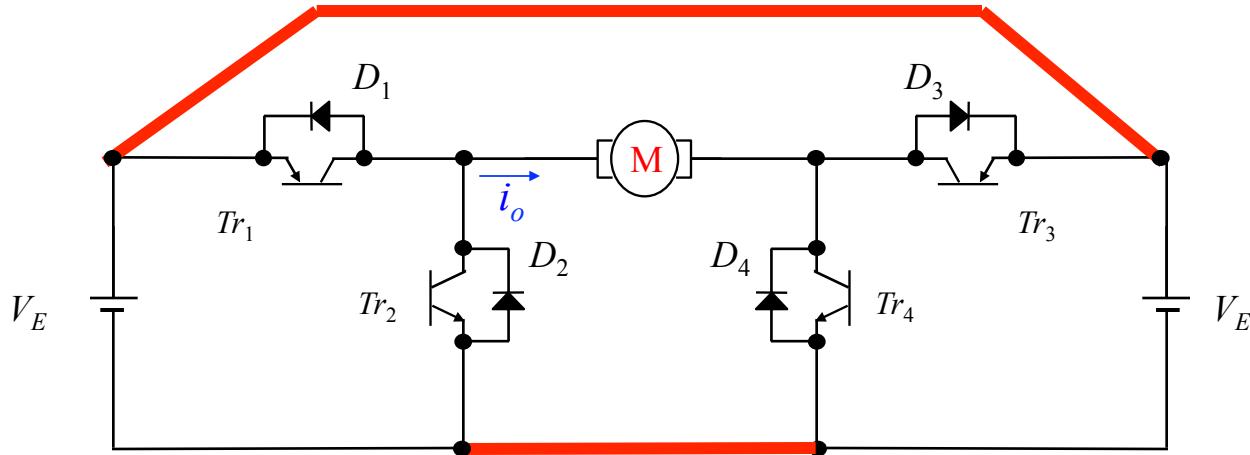


(a) Chopper for forward driving/braking

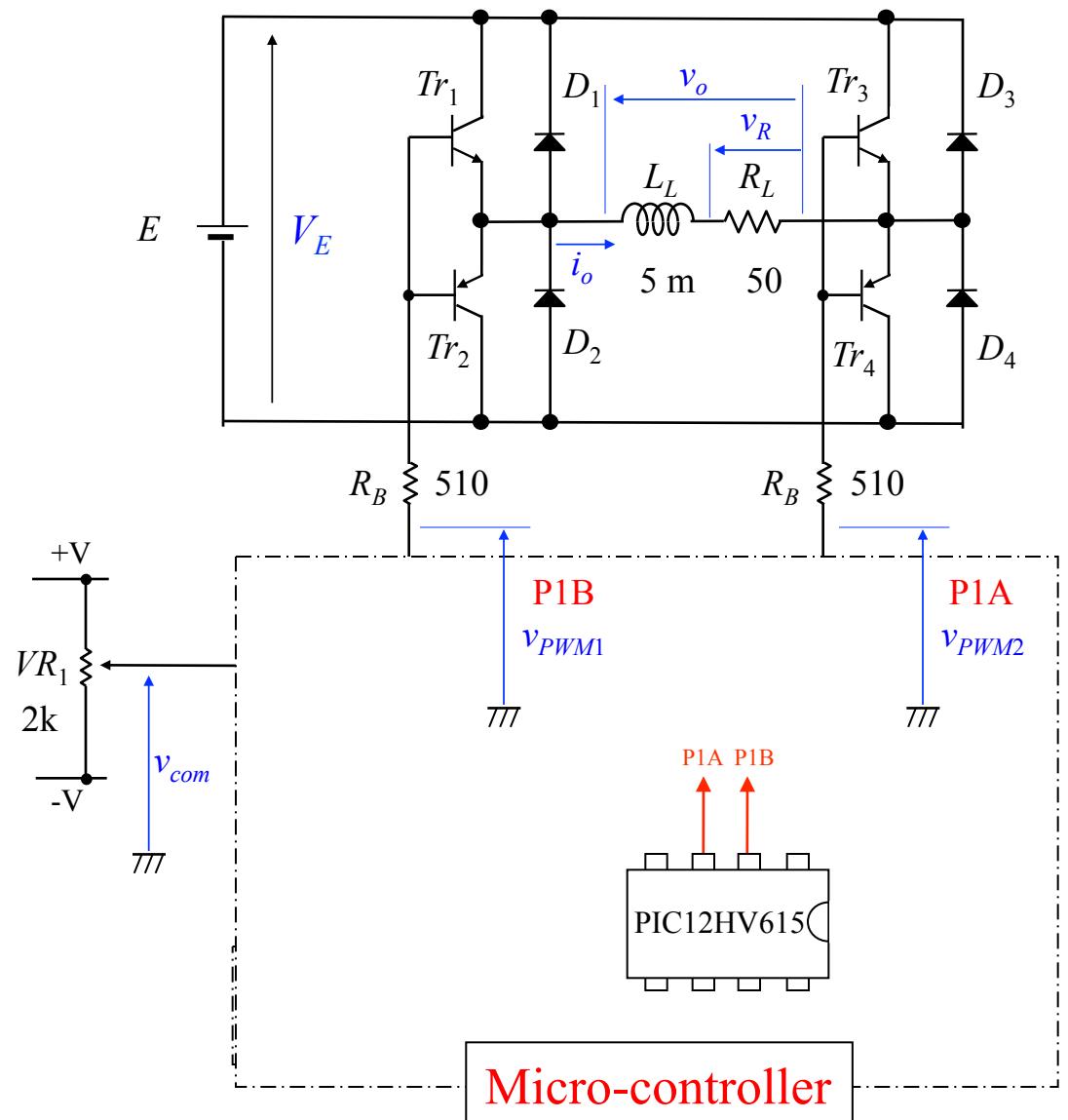


(b) Chopper for backward driving/braking





Full-bridge inverter



If $v_{com} \geq v_{tri}$

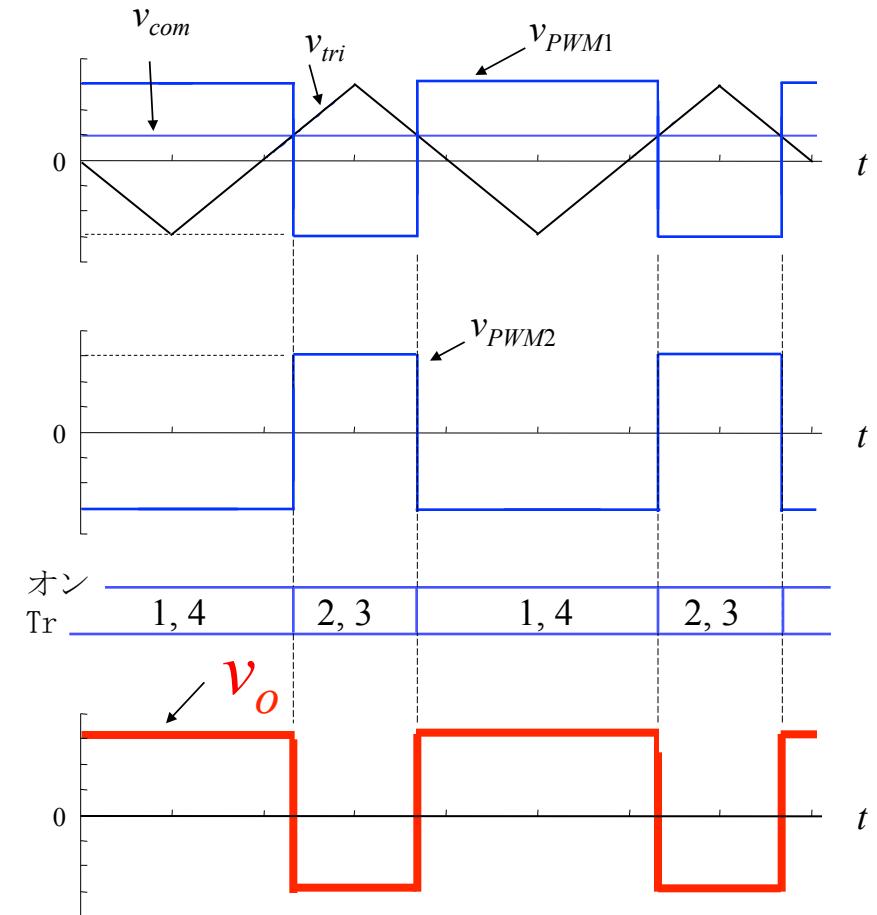
Tr_1 ON, Tr_3 OFF
 Tr_2 OFF, Tr_4 ON

If $v_{com} < v_{tri}$

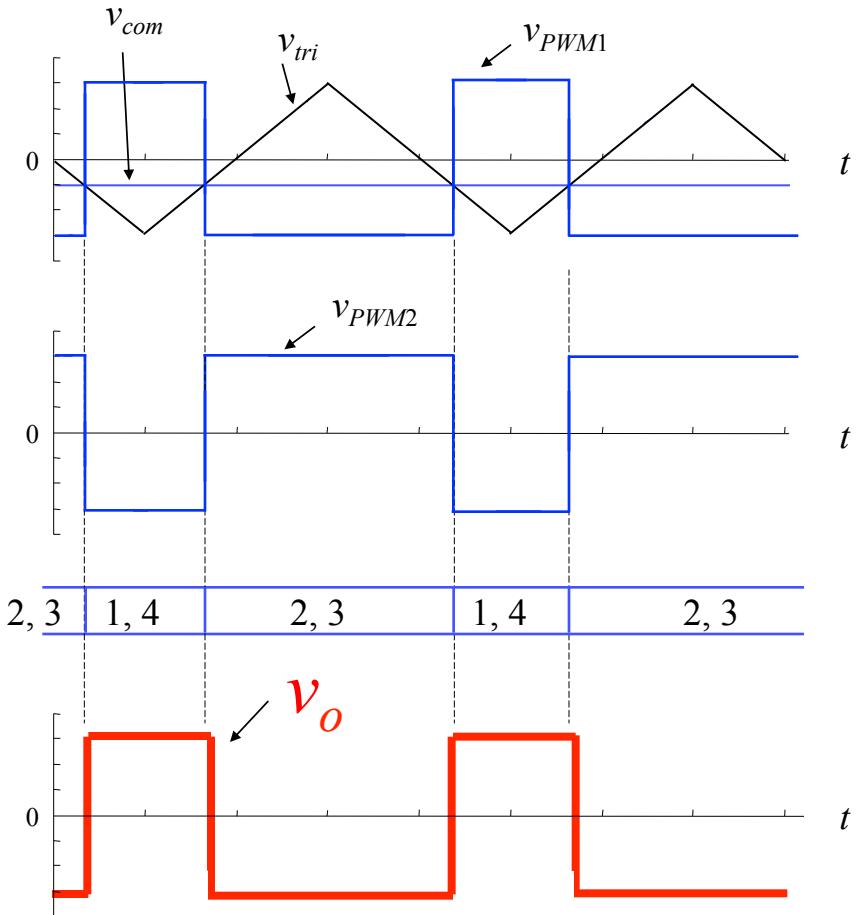
Tr_1 OFF, Tr_3 ON
 Tr_2 ON, Tr_4 OFF

(10.1)

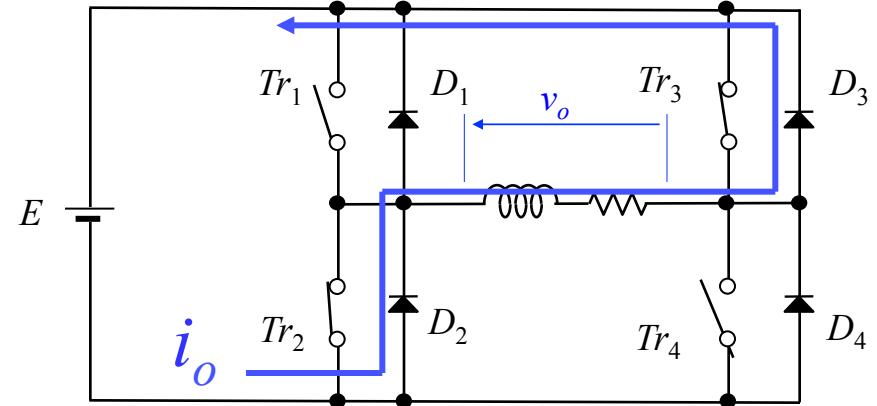
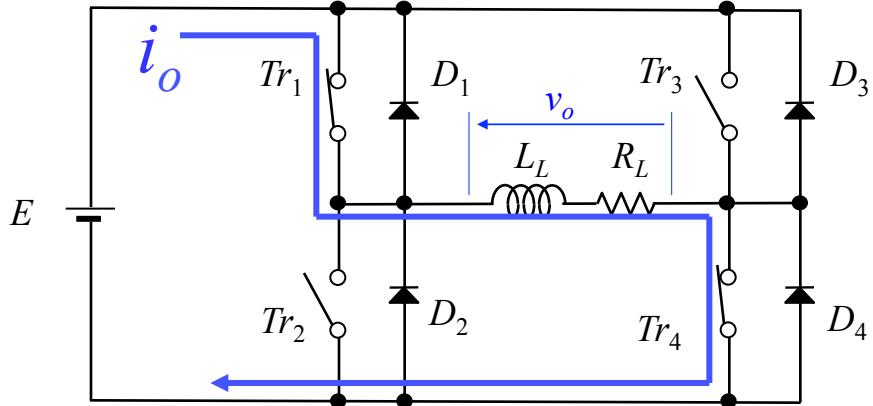
Full bridge inverter and controller



PWM control scheme and output voltage
($v_{com} > 0$)

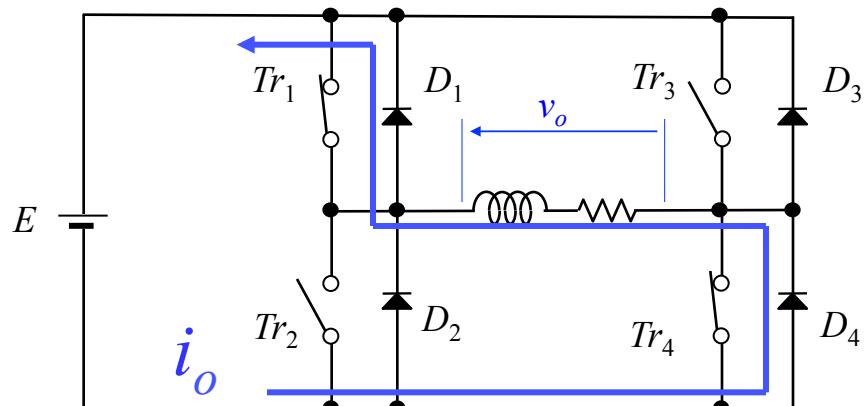


PWM control scheme and output voltage
($v_{com} < 0$)

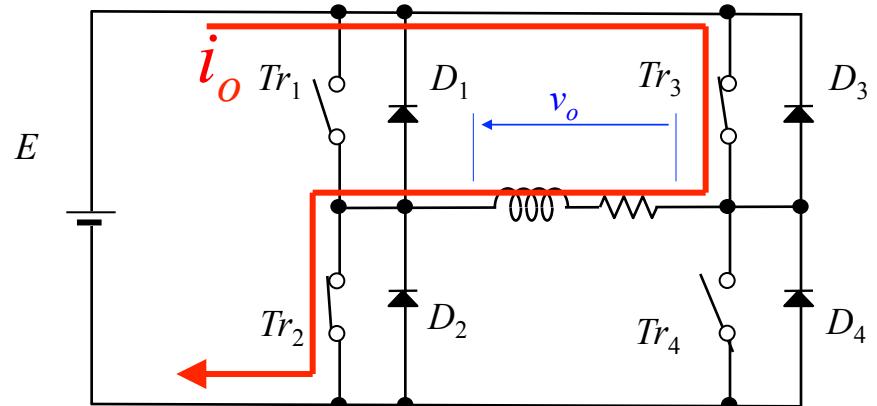


(a) $i_o > 0$, Tr_1 , Tr_4 :ON, Tr_2 , Tr_3 :OFF, $\mathcal{V}_o = V_E$

(b) $i_o > 0$, Tr_1 , Tr_4 :OFF, Tr_2 , Tr_3 :ON, $\mathcal{V}_o = -V_E$

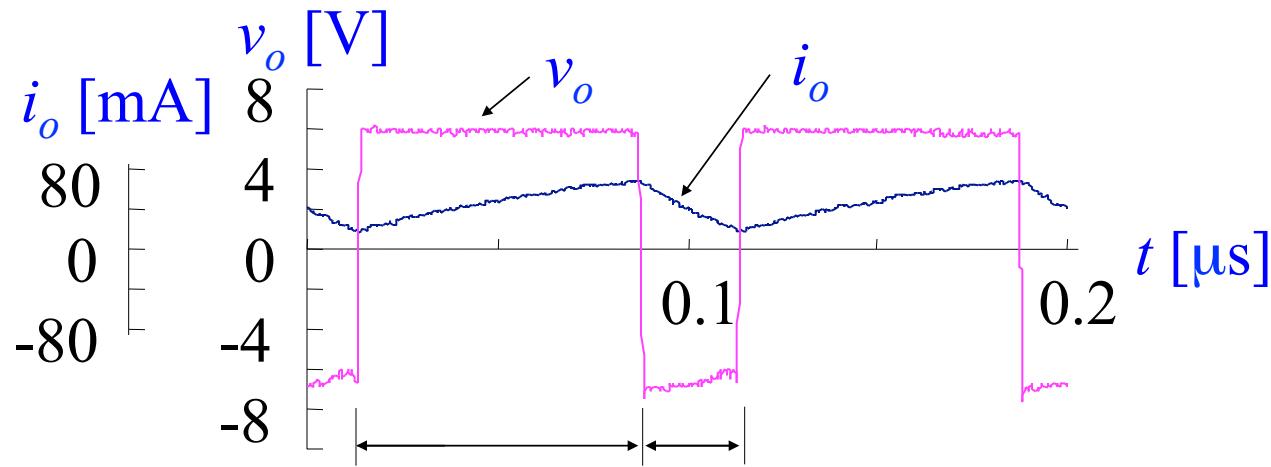


(c) $i_o < 0$, Tr_1 , Tr_4 :ON, Tr_2 , Tr_3 :OFF, $\mathcal{V}_o = V_E$

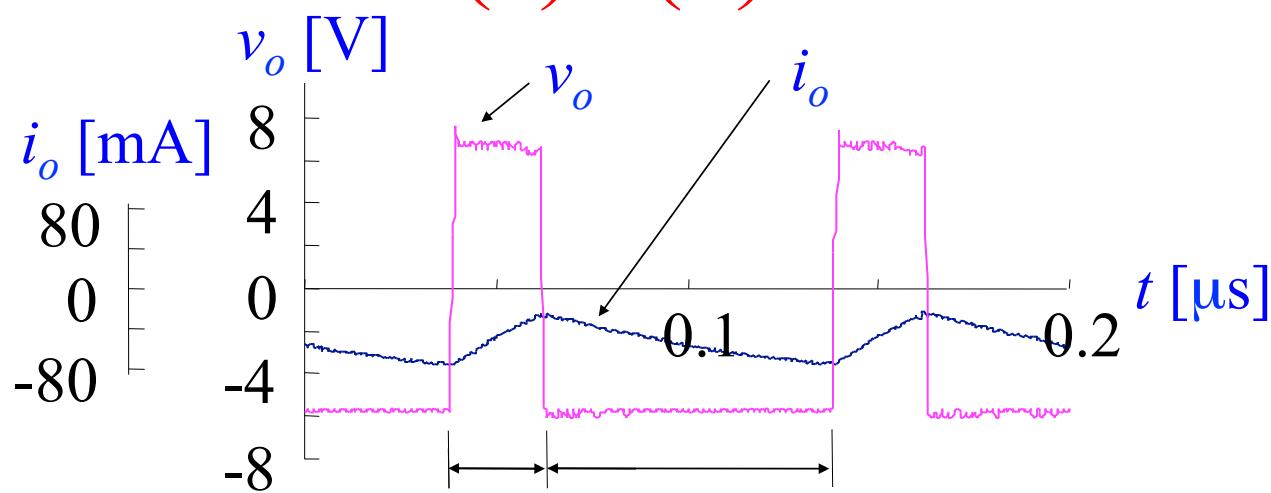


(d) $i_o < 0$, Tr_1 , Tr_4 :OFF, Tr_2 , Tr_3 :ON, $\mathcal{V}_o = -V_E$

Four modes of the PWM control scheme



(a) (b)

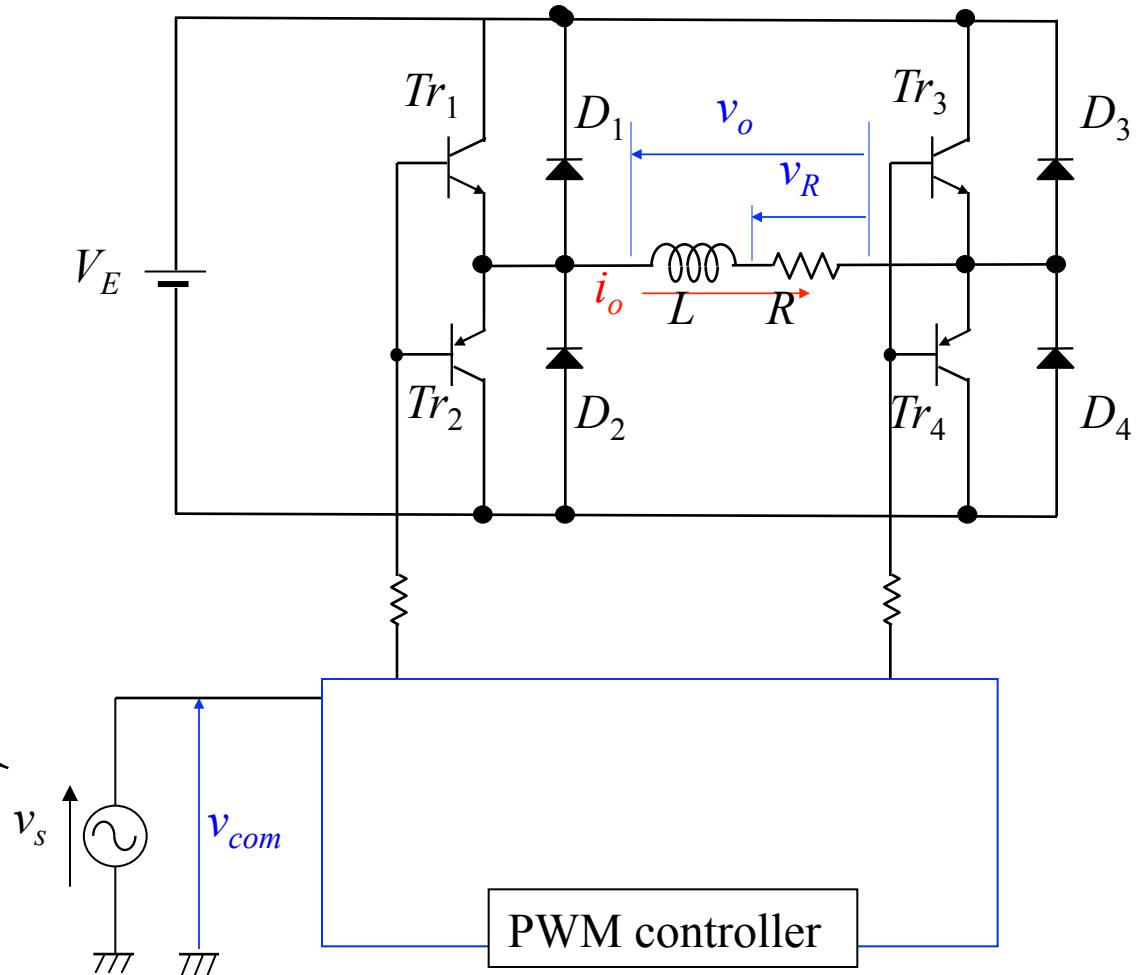


(c) (d)

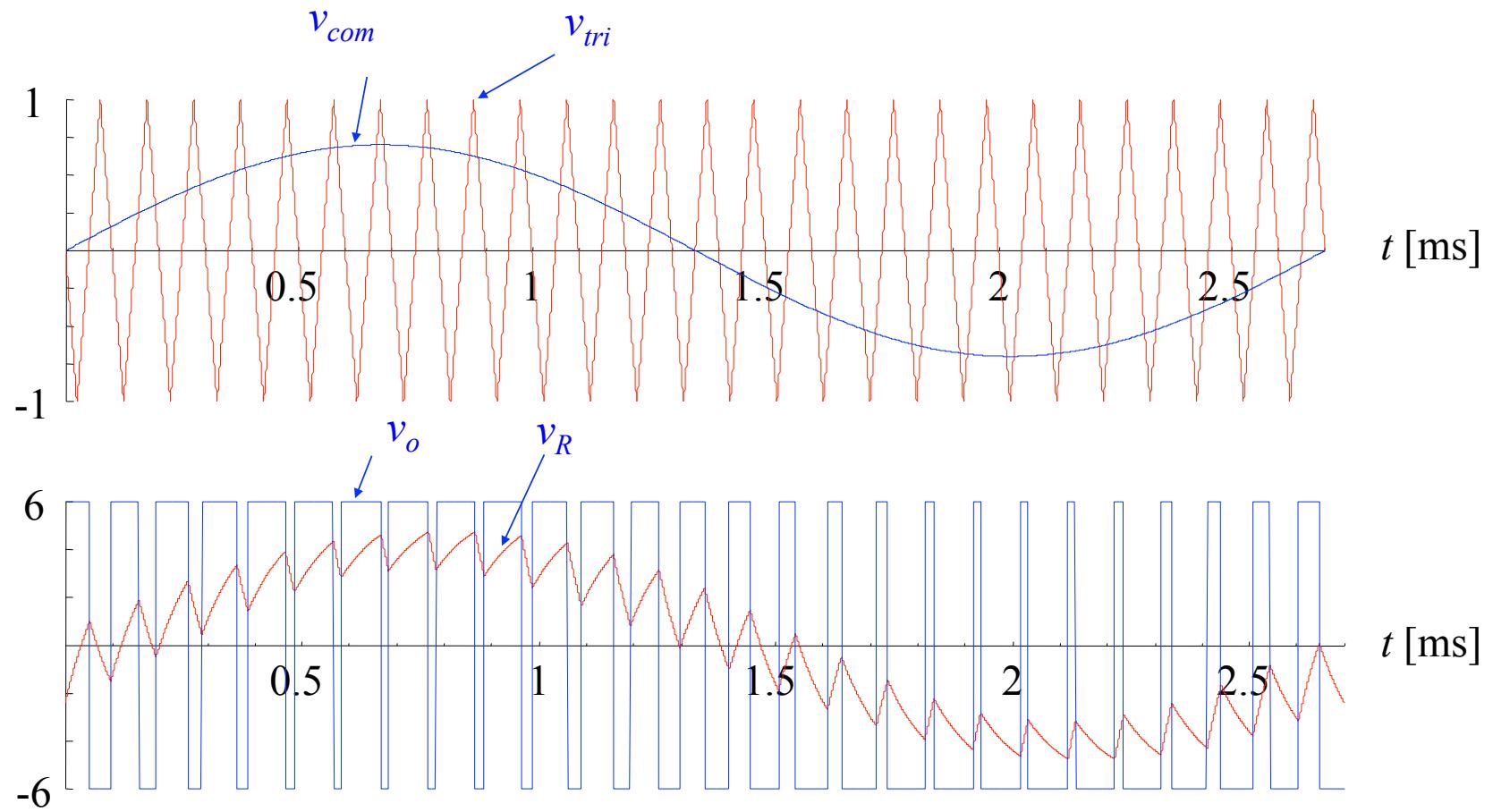
Output voltage and current of the PWM inverter

AC inverter

AC signal
can be
applied.



AC voltage output by a full-bridge inverter

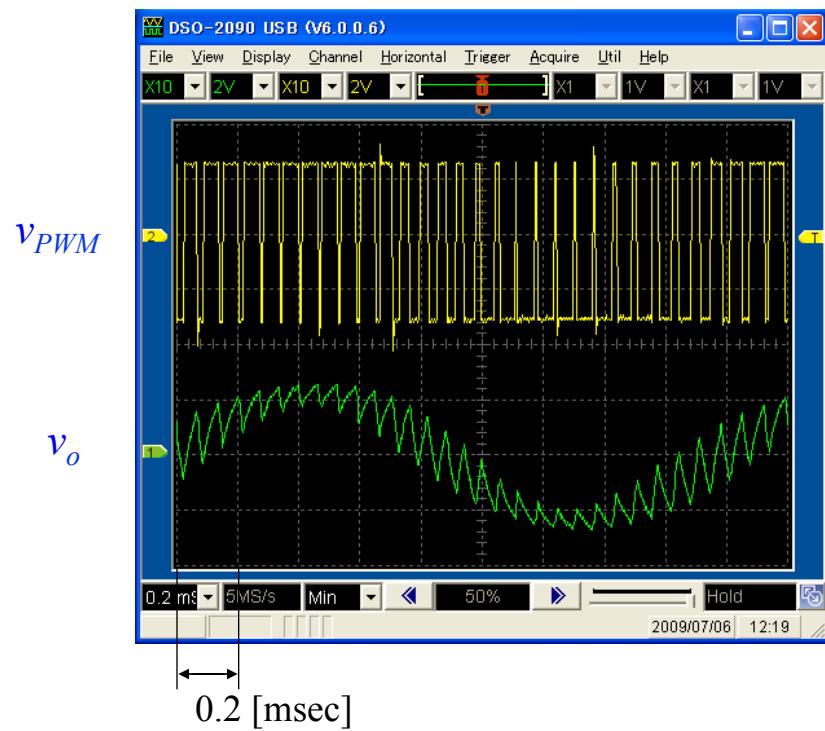
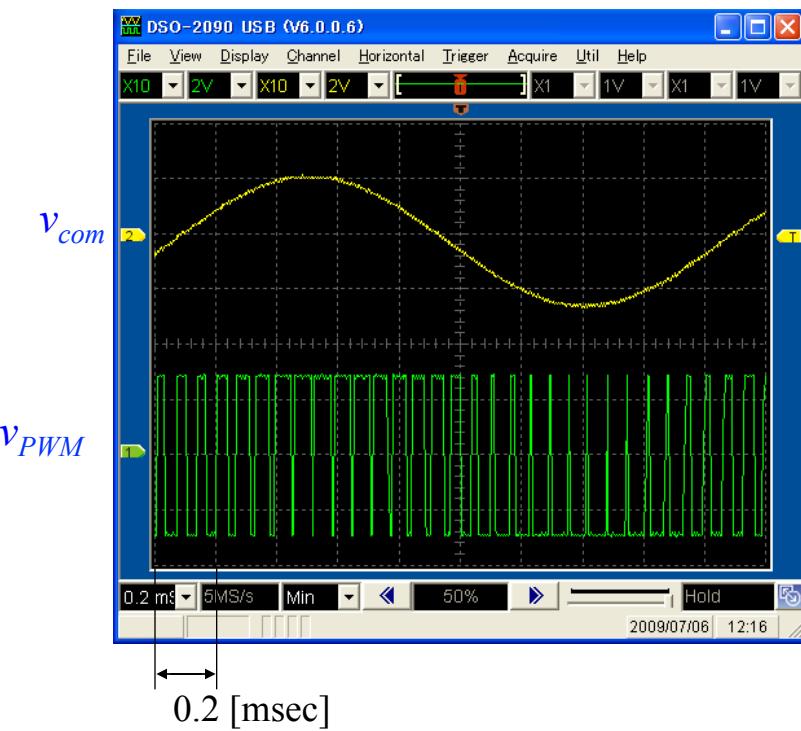
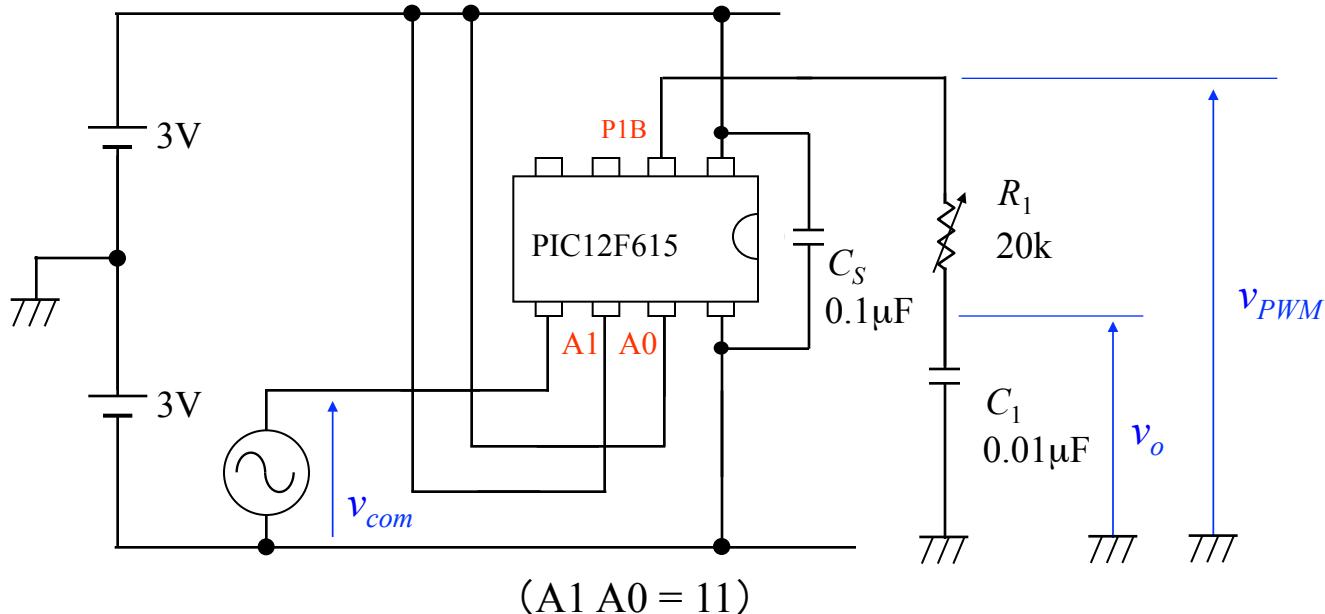


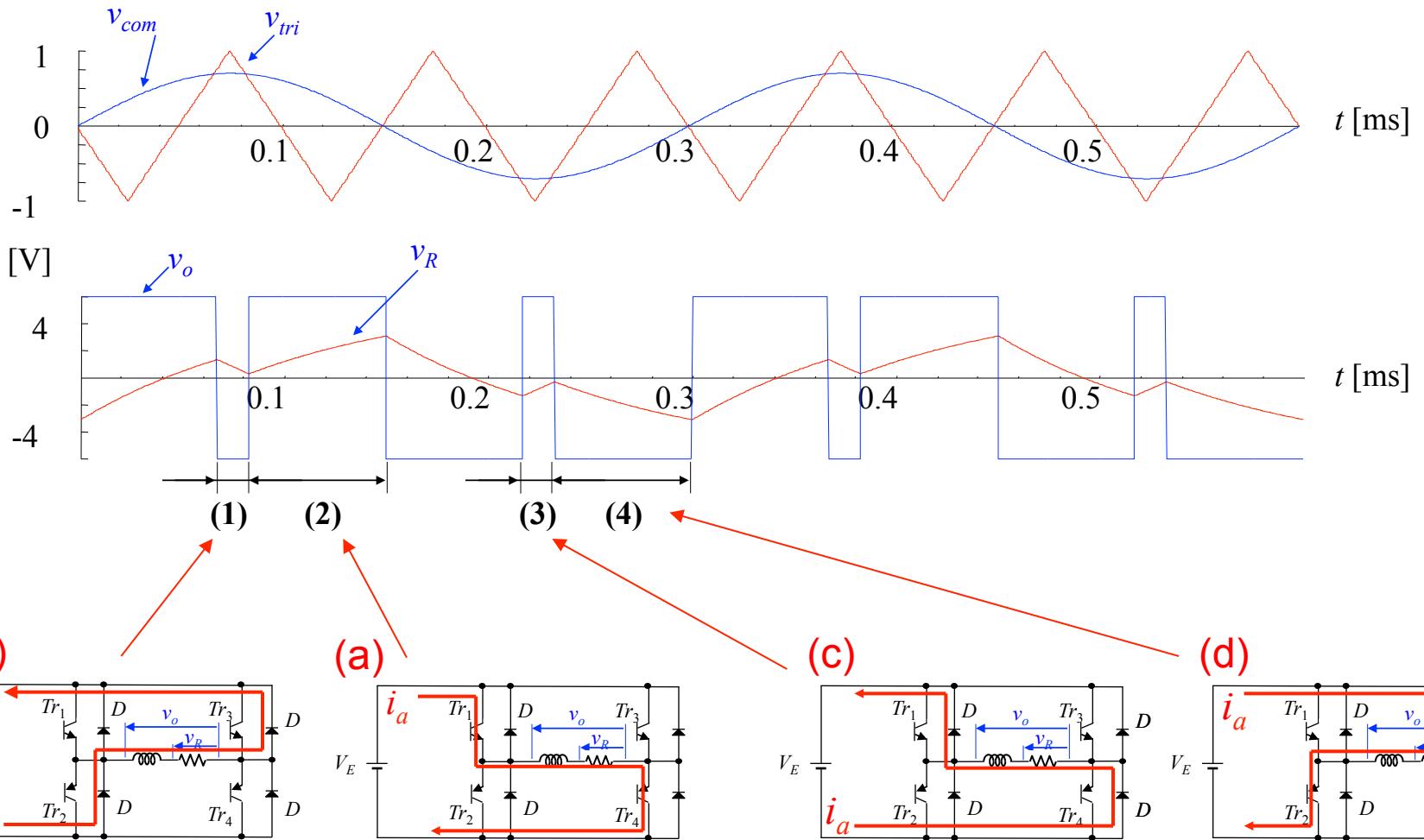
Output voltage v_o and voltage across resister v_R ($f_{com} / f_{tri} = 1/27$)

Experiment

$$f_{com}/f_{tri} = 1/27$$

$$f_{com} = 555 \text{ [Hz]}$$





Four modes of the PWM control scheme