

並行分散計算特論 (2)

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Alternative Semantics

Communicating process is more distinguishable than classic automata
(Automata theory = Language)

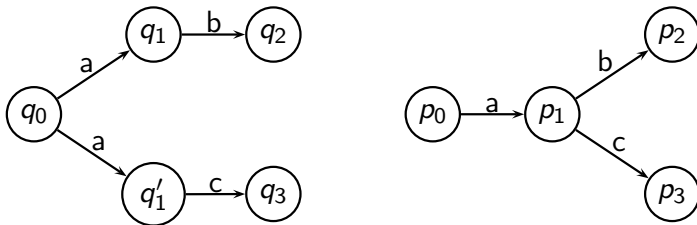
Communication enables to observe intermediate states

What could be the alternative semantics for languages?

Example 3.2

Essential difference between B_1 and B_2

Nondeterminism after $2p$



a = insert a coin b = get coffee c = get tea

Labelled Transition System

· LTS = Automaton - FinalStates - InitialState

Definition

LTS

An LTS over Act: (Q, T)

Q : Set of states

T : Transitions

$$q \xrightarrow{a} q' \text{ for } (q, a, q') \in T$$

LTS as Behavioral Model

Principles:

- Communication is the only way to know what a communication process is;
- There is no way to know if a communicating process is in the initial state nor in the final states.
- Reaction pattern of communications = **semantics of communicating processes**

Realized as an (equivalence) relation over LTS states

Strong simulation

A class of relations over LTS states

Definition

S is a simulation:

For all $(p, q) \in S$ and $a \in Act$, $p \xrightarrow{a} p'$ implies that there exists q' such that $q \xrightarrow{a} q'$ and $(p', q') \in S$.

If there is a simulation that includes (p, q) , it is said that q strongly simulates p or p is strongly simulated by q

$$\begin{array}{ccc} p & \xrightarrow{a} & \forall p' \\ S & & S \\ q & \xrightarrow{a} & \exists q' \end{array}$$

Intuitive meanings of simulation

q (strongly) simulates p

q is more capable in the communications than p at each point of communication from now on

p can be more nondeterministic than q

Example 3.4

$\{(q_0, p_0), (q_1, p_1), (q'_1, p_1), (q_2, p_2), (q_3, p_3)\}$

p_0 (strongly) simulates q_0

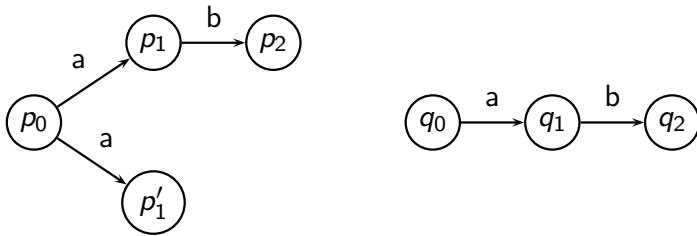
Strong bisimulation

Definition

Given an LTS (Q, T) , a simulation S over Q :

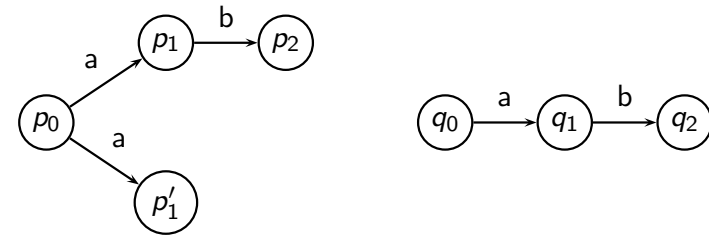
S is a strong bisimulation if S and S^{-1} are both strong simulations.
 $p \sim q$ if there exists a strong bisimulation S such that $(p, q) \in S$

Bismularity



p_0 simulates q_0 and q_0 simulates p_0
 But p is not bisimilar to q

Bismularity



p_0 simulates q_0 and q_0 simulates p_0
 But p is not bisimilar to q
 Two simulations are not in the reversal relation.

Properties of \sim

- \sim is an equivalence (Prop.3.9)
- \sim is a strong bisimulation (Prop.3.9)

Moreover, \sim is the largest strong bisimulation.

Sequential process expression

$$P ::= A\langle a_1, \dots, a_n \rangle \mid \sum_{i \in I} \alpha_i.P_i$$

I : finite index set
 for each A : $A(\vec{a}) \stackrel{def}{=} P_A$

Process identifier A is for recursion

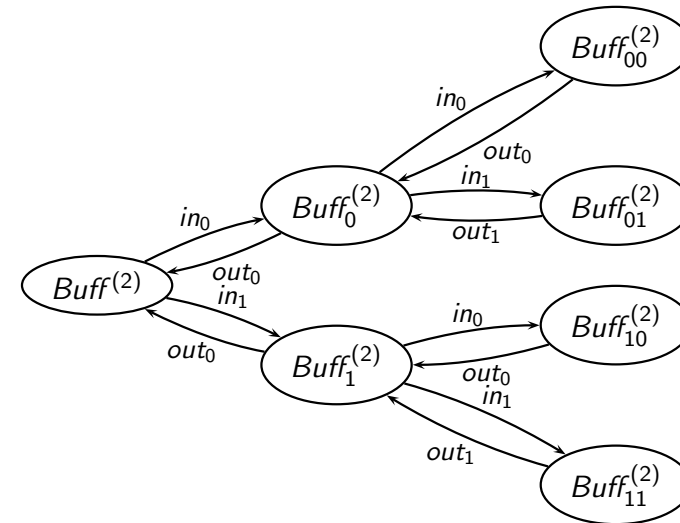
Structural congruence

- Alpha conversion for bound names
- A-C property of operators (Choice)

\equiv is the trivial equivalence

We consider the quotient of structural congruences over LTS

Boolean Buffer



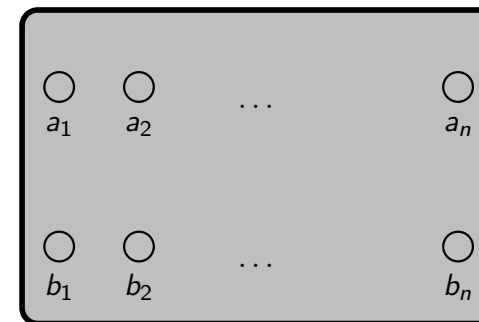
Expression of Boolean Buffer

$$Buff^{(2)} \stackrel{def}{=} \sum_{i \in \{0,1\}} in_i . Buff_i^{(2)}$$

$$Buff_i^{(2)} \stackrel{def}{=} \overline{out_i} . Buff^{(2)} + \sum_{j \in \{0,1\}} in_j . Buff_{ji}^{(2)}$$

$$Buff_{ij}^{(2)} \stackrel{def}{=} \overline{out_j} . Buff_i^{(2)}$$

Scheduler specification



a_i : Job_i starts
 b_j : job_j ends

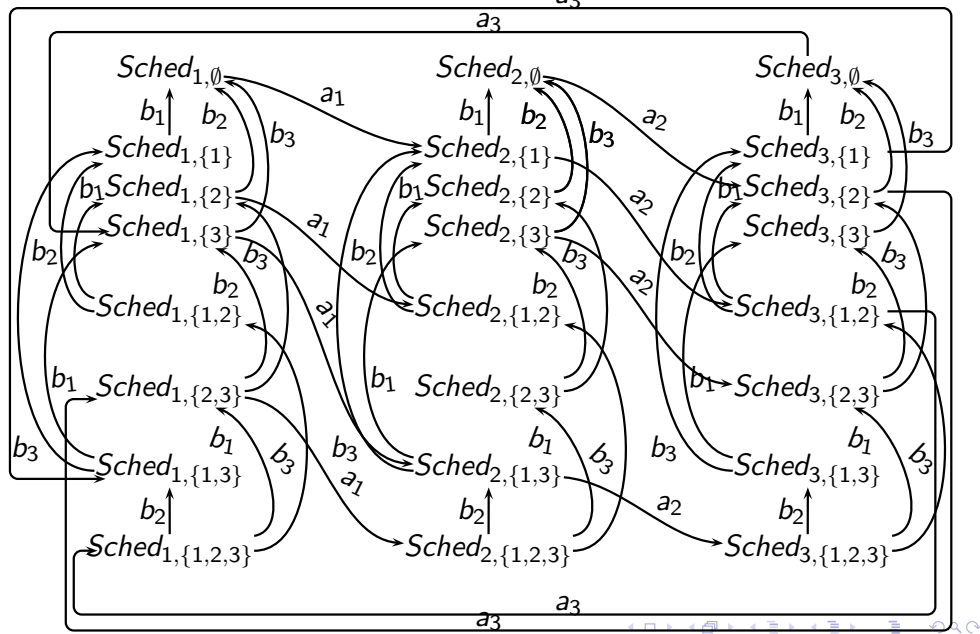
Each job can be invoked only once at a time.

a_i is unavailable without pushing b_i .

A job can be overtaken by other jobs.

You can push b_j before b_i ($i < j$) is pusehd.

Scheduler (n=3)



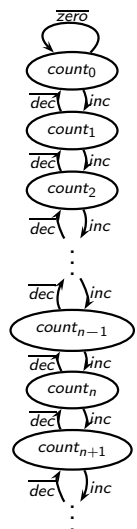
Expression of scheduler

$$\text{Scheduler} \stackrel{\text{def}}{=} \text{Sched}_{1,\emptyset}$$

$$\text{Sched}_{i,X} \stackrel{\text{def}}{=} \begin{cases} \sum_{j \in X} b_j \cdot \text{Sched}_{i,X \setminus j} & i \in X \\ \sum_{j \in X} b_j \cdot \text{Sched}_{i,X \cup j} + a_i \cdot \text{Sched}_{i+1,X \cup i} & i \notin X \end{cases}$$

$\text{Sched}_{i,X}: a_i$ is in turn. X is the set of jobs whose b action is not yet done.

Counter



$$\text{Count}_0 \stackrel{\text{def}}{=} \text{inc} \cdot \text{Count}_1 + \overline{\text{zero}} \cdot \text{Count}_0$$

$$\text{Count}_{n+1} \stackrel{\text{def}}{=} \text{inc} \cdot \text{Count}_{n+2} + \overline{\text{dec}} \cdot \text{Count}_n$$

Counter is not in the regular class.
 (More expressive than the sequential processes)
 Infinite number of process identifiers.