

## Timed regular language

A timed language  $L$  is *timed regular* if  $L$  is accepted by a timed Büchi automaton.

Examples of non-regular languages:

$$\begin{aligned} & \{((ab)^\omega, \tau) \mid \tau_{4i-2} - \tau_{4i-3} < \tau_{4i} - \tau_{4i-1}\} \\ & \{((a)^\omega, \tau) \mid \tau_i = 2i\} \end{aligned}$$

No memory in time

## 並行分散計算特論 (12)

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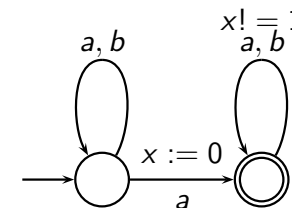
## Closure property

**Untimed case:** A regular language is closed under *union, intersection, complement, (determinization)*

**Timed case:** Closed under *union, intersection*  
Complementation: Generally No  
Determinization: Generally No

## Complementation

Timed Büchi automata not closed under complementation.



Sequences not accepted:  $\exists i \exists j. i < j \wedge (a, \tau_i) \wedge \tau_j - \tau_i = 1$   
Must check infinitely many pairs of  $(i, j)$  of either  $(a, \tau_i)$  and  $(a, \tau_j)$ , or  $(a, \tau_i)$  and  $(b, \tau_j)$ .

This cannot be implemented with finite states.

## If complementation is closed

Language inclusion problem:

$$L_1 \subseteq L_2 \text{ is equivalent to } L_1 \cap \overline{L_2} = \emptyset$$

What is the subclass closed under complementation?

## Verification

Check if the behavior of implementation satisfies the desired property.

**Safety:** A system does nothing wrong.

**Liveness:** A system does right thing.

**Formal language approach:**

Safety:  $L(I) \subseteq L(S)$

Liveness:  $L(I||S) \neq \emptyset$

## Emptiness Checking

Set the state as a final state. Check if the language is empty or not.

### Definition

For  $\mathcal{A}$ , check if its timed language  $L(\mathcal{A})$  is empty or not.

Decidable in PSPACE complete.

Exponential wrt the number of clocks

## Clock region

Time constraint:  $\delta ::= x \leq c | c \leq x | \neg \delta | \delta_1 \wedge \delta_2$   
where  $c \in \mathcal{Q}$ .  $\mathcal{Q}$ :Rational numbers.

### Definition

$\nu \sim \nu'$  if

(1)  $\forall x. \lfloor \nu(x) \rfloor = \lfloor \nu'(x) \rfloor$  or  $(\nu(x) \geq c_x \text{ and } \nu'(x) \geq c_x$

(2)  $\forall x, y.$ , when  $\nu(x) \leq c_x$  and  $\nu(y) \leq c_y$   
 $\text{fract}(\nu(x)) \leq \text{fract}(\nu(y))$  iff  $\text{fract}(\nu'(x)) \leq \text{fract}(\nu'(y))$

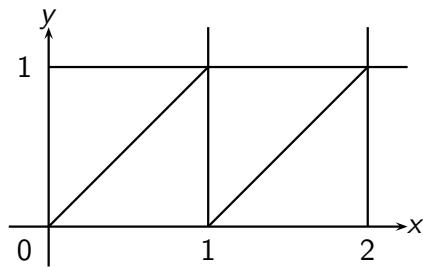
(3)  $\forall x. \text{fract}(\nu(x)) = 0$  iff  $\text{fract}(\nu'(x)) = 0$

$c_x$  : biggest constant in  $\delta$  of  $x$

### Definition

Clock region:  $I_c / \sim$  where  $I_c = [C \rightarrow R_{\geq 0}]$

## Clock region



$$c_x = 2, c_y = 1$$

6 corner points  
14 open line segments  
8 open regions

Rational numbers  $\rightarrow$  Integers

For a region  $\alpha, \alpha \models \delta$  if for all  $\nu \in \alpha, \nu \models \delta$

upper bound of regions:  $|C|! \cdot 2^{|C|} \cdot \prod_{x \in C} (2c_x + 2)$

$c_x$ : maximum constant in time constraints of  $x$

## Region automaton

$R(\mathcal{A})$  Region automaton of  $\mathcal{A}$

Definition

$$\mathcal{A} = \langle \Sigma, S, S_0, C, E \rangle, R(\mathcal{A}) = \langle \Sigma, S_R, S_{R0}, E_R \rangle$$

$$S_R = \{(s, \alpha) \mid s \in S, \alpha \text{ is a clock region}\}$$

$$S_{R0} = \{(s_0, [\nu_0]) \mid s_0 \in S_0\}$$

$$E_R = \{ \langle (s, \alpha), a, (s', \alpha') \rangle \mid \langle s, s', a, \lambda, \delta \rangle \in E$$

$$\exists \alpha'' . \alpha'' \text{ is a time successor of } \alpha, \alpha'' \models \delta \text{ and } \alpha' = [\lambda \rightarrow 0] \alpha \}$$

## Time successors

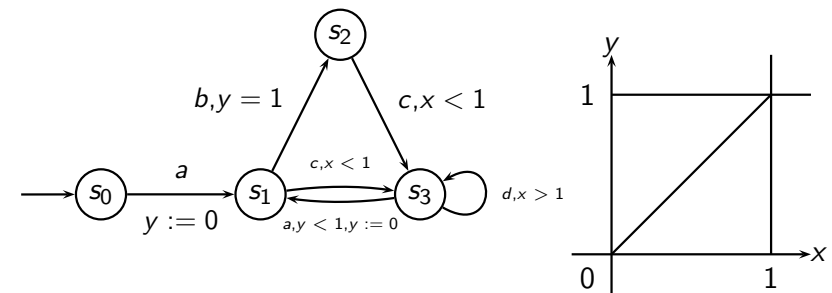
Definition

$\alpha'$  is a time successor of  $\alpha$  if

$$\forall \nu \in \alpha. \exists t \in R. \nu + t \in \alpha'$$

For a time successor  $\beta$  of  $\alpha$ , if  $\alpha \models \delta$  implies  $\beta \models \delta$ , then  $\beta$  is identified with  $\alpha$ .

## Example



$\langle s_0, a, T, \{y\}, s_1 \rangle$ :

time successor of  $[x = y = 0]$ :

$[0 < x = y < 1], [x = y = 1], [x > 1, y > 1]$

time successor: region reached by time passage

# Region automata

